

Silicon NPN Power Transistors

2SC2361

DESCRIPTION

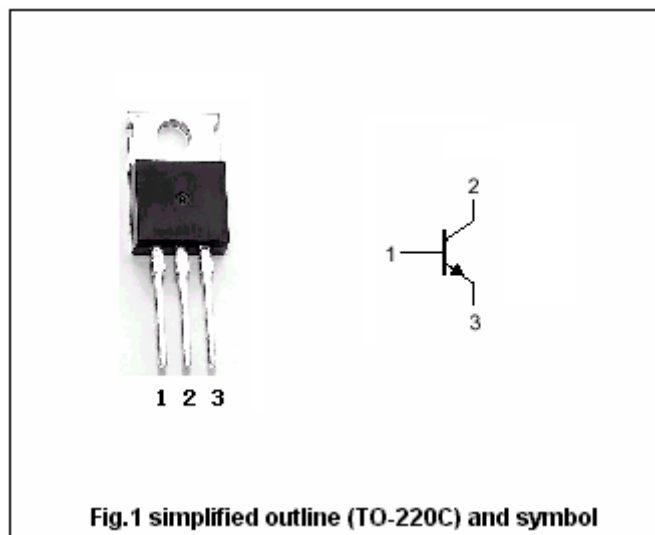
- With TO-220C package
- Complement to type 2SA1123
- Low collector saturation voltage

APPLICATIONS

- For power amplifier applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

Absolute maximum ratings($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	100	V
V_{CEO}	Collector-emitter voltage	Open base	70	V
V_{EBO}	Emitter-base voltage	Open collector	6	V
I_C	Collector current (DC)		4	A
P_C	Collector dissipation	$T_C=25^\circ\text{C}$	25	W
T_j	Junction temperature		150	$^\circ\text{C}$
T_{stg}	Storage temperature		-50~150	$^\circ\text{C}$

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE0(SUS)}	Collector-emitter sustaining voltage	I _C =0.1 A; I _B =0	70			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1m A; I _C =0	6			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =4A; I _B =0.4A			1.5	V
V _{BE}	Base-emitter on voltage	I _C =3A; V _{CE} =4V			2.0	V
I _{CBO}	Collector cut-off current	V _{CB} =100V; I _E =0			10	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =6V; I _C =0			10	μ A
h _{FE}	DC current gain	I _C =2.5A; V _{CE} =3V	50		240	
f _T	Transition frequency	I _C =0.5A; V _{CE} =10V		70		MHz

PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance: ±0.10mm)